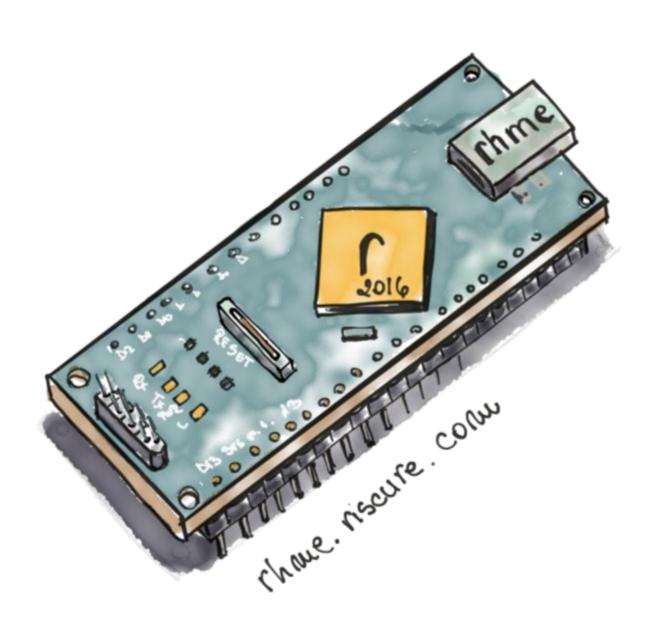
# riscure



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@esanfelix

#### Shout-out to the team!



























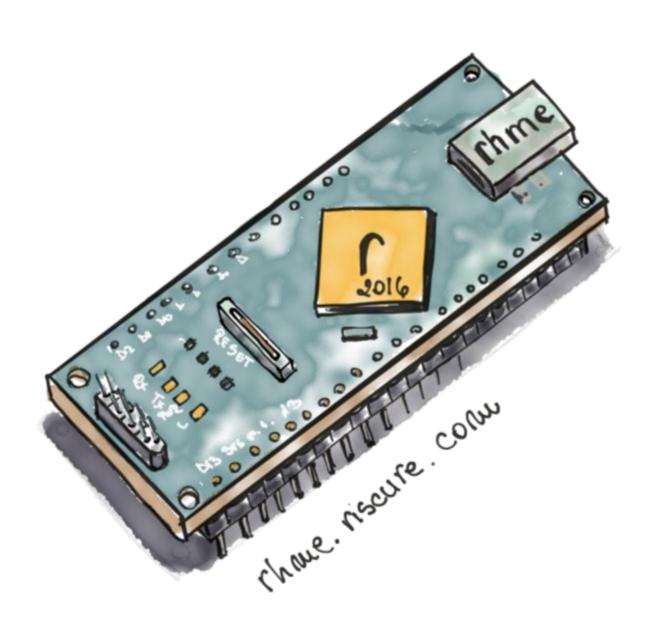






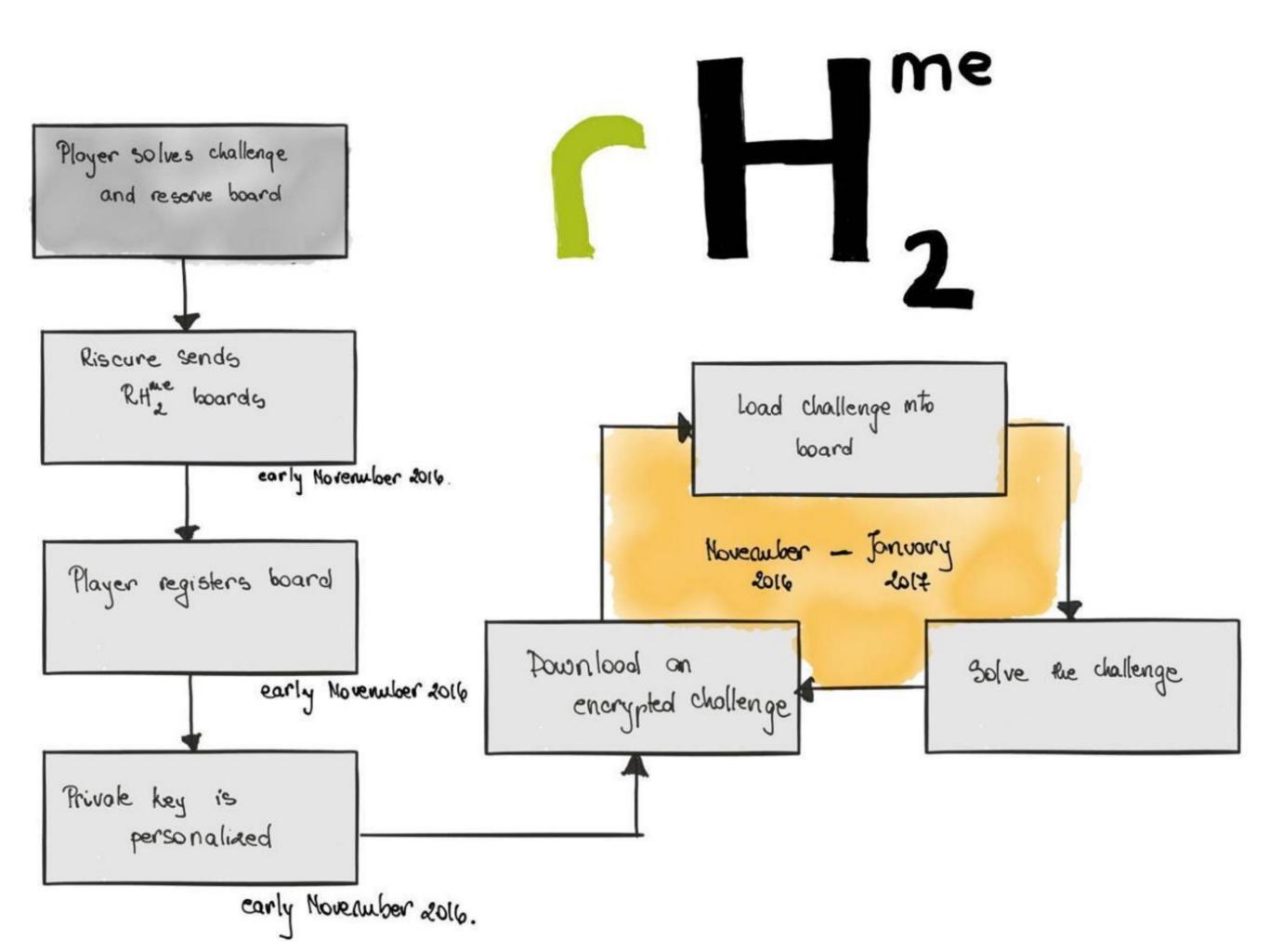
#### RHme2 you say?





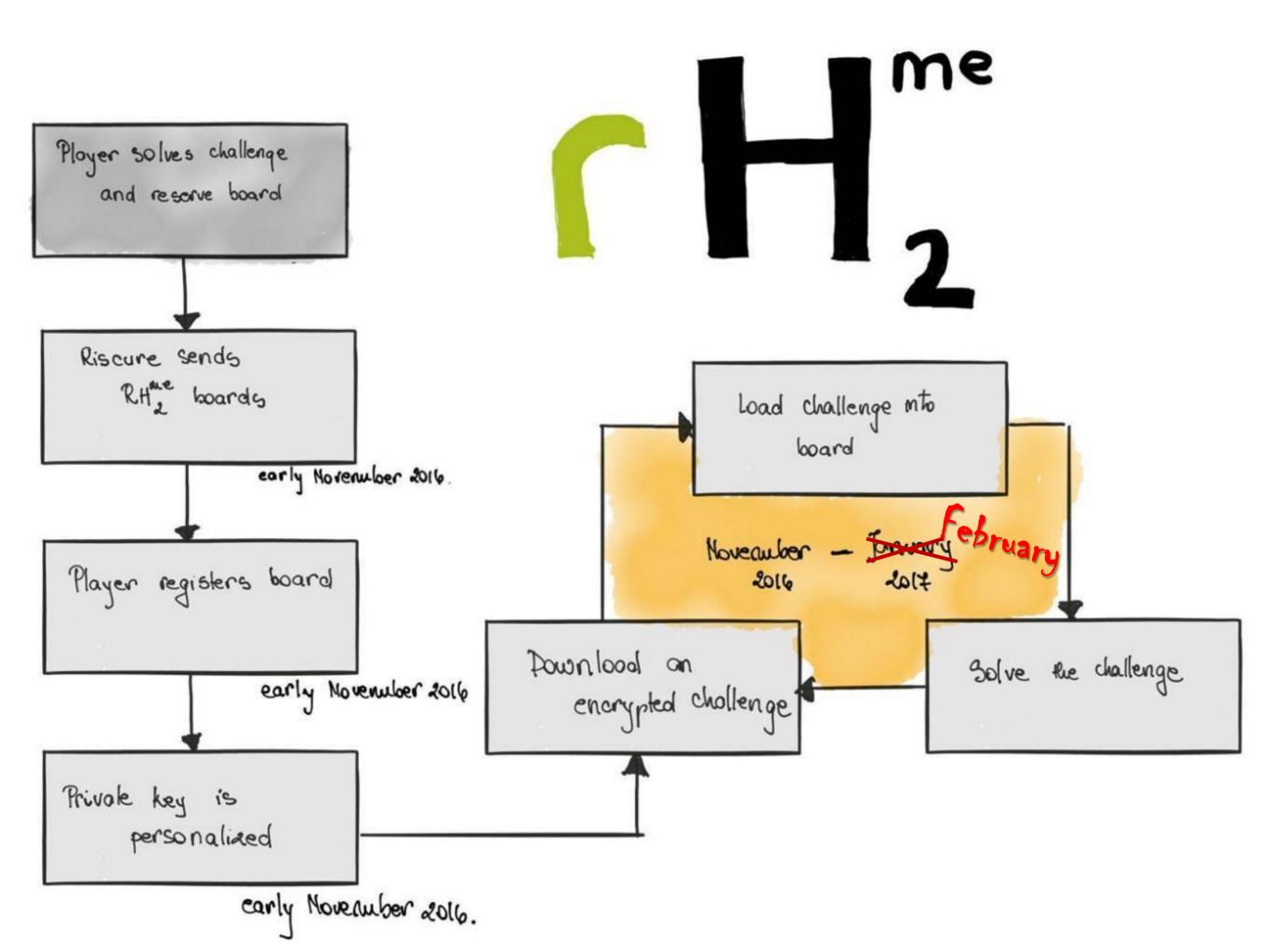
#### Embedded hardware CTF

- Usual types of challenges
- Side Channel Analysis
- Fault Injection
- Other PCB-related challenges













#### FridgeJIT

A senior technical manager of a fridge manufacturer demanded the ability to update the firmware in their new product line (we need to monitor and control the temperature, right?) of all deployed devices over the air and without user interaction. This way, the manufacturer could improve the user experience by providing firmware updates, even when the fridge is 1 or 2 years old.

It turned out that the CPU that comes with the fridges does not allow self-upgrading the firmware, so the developers built a VM for the fridge software which at that time was just a few lines of code. Incidentally, half of the development and test team was fired 2 months after releasing the new product line.

A crafty customer has been able to reverse engineer the software and programmed the fridge with different software. His goal was to build a digital safe, but the guy claims not being able to make the application small enough to fit inside the VM. However, to be sure we ask you to check whether this is correct.

Are you able to crack the password? We have been able to extract the full firmware image of a slightly different fridge and a memory dump of their fridge. We hope this is enough...

Note: The flag is in a different format than usually...

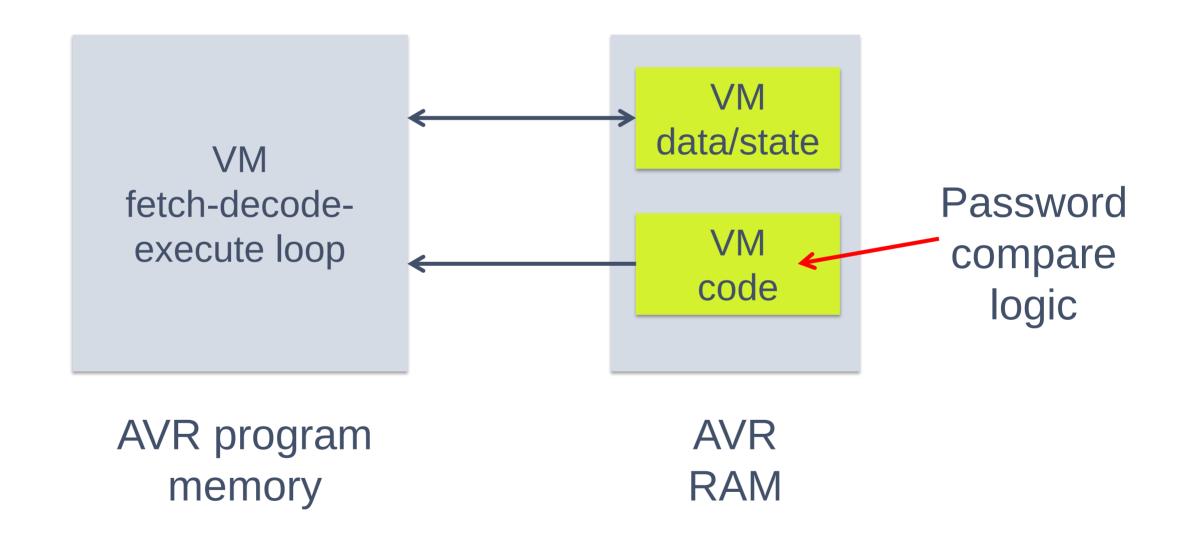
Challenge

firmware.bin

memory.dmp

#### Challenge setup





## FridgeJIT console → disassembly\*



```
FridgeJIT Console ]
>> 0000: 05002500 MOVH r0 #2500
                                    R0: 00000000 R4: 00000000
  0004: 0400203a MOVL r0 #203a |
                               R1: 00000004 R5: 00000001
  0008: 0100 PUSH r0
                               | R2: 00000011 SP: 00000010
  00: 04006f77 MOVL r0 #6f77 | . | Ż: 0
```

Appeared after pressing ^C enough times... or when loading bytecode to Weird Machine

\*Some unsupported opcodes!

# After a bit of reversing...



- A few check routines, 4 bytes at a time
- Simple arithmetic for each of them, for example:

 $rol(input,17)^0x3d6782a5 == 0x5dd53c4f$ 

Flag easily recovered from these expressions



#### Hide & Seek

So you found the password last time? This time it got a little bit harder. Instead of hiding it in the VM, it is somewhere else on the device. Are you able to find it?



# This time you had to read it out from memory!

#### Flawed opcode handlers

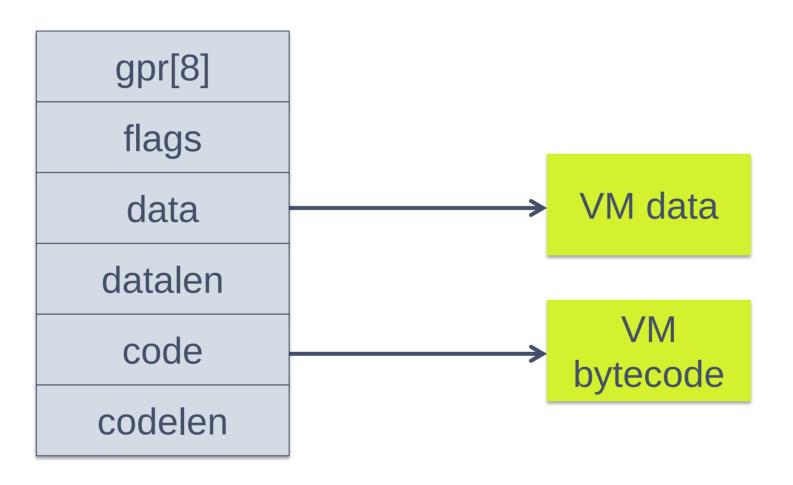


```
#define GET_REG(vm,r) (vm->gpr[r])
#define XOR_REG(vm,r,v) (vm->gpr[r] ^= v)
#define AND_REG(vm,r,v) (vm->gpr[r] &= v)
#define OR_REG(vm,r,v) (vm->gpr[r] |= v)
#define NOT_REG(vm,r) (vm->gpr[r] = ~vm->gpr[r])
```

Obvious lack of bound checks!

# **Exploitation?**





Plan: modify *data* to get arbitrary read/write using LOAD/STORE instructions

#### Payload to dump all SRAM



```
def read all():
    \# r4 = 0x100 << 8, r5 = 0x100 (1 << 8)
    x = set_reg(4, 0x00000000) + movl(5, 0x100)
    looptgt = len(x)/2
    # zero out the address
    x += xor(0,0) + andr(8,0)
    # XOR with reg 4, contains current addr
    x += xor(8, 4)
    # Now read into R3
    x += xor(3, 3)
    x += load(3, 0) + out(3)
    # Inc address by 0x100, so goes to next byte
    x += add(4, 5)
    x += movl(7, looptgt-4)
    return x
```

SRAM dump reveals flag



#### The Weird Machine

Damn fridges. It seems there is no end to the problems they bring. And this time time it got even more difficult. I guess you already know in which direction this goes, right?

**Challenge** 

Memory dump not enough, need code execution!

## Arbitrary write to code exec?



- Standard ROP
  - 1. Find a stack pivot
  - 2. Replace *opcode handler* function pointer
  - 3. Trigger

- ROP a-la AVR:
  - 1. Write ROP chain to memory
  - 2. Write address to SP (0x5D:0x5E)

#### **Unexpected flaw (HydraBus, Balda)**



```
void do_call_reg(vm_state *vm) {
    uint8_t reg = GET_REGA(vm);

//Check stack pointer validity
    if(check_ptr(vm, GET_REG(vm,ESP))) {
        SUB_REG(vm, ESP, 4);

        //First we push the return address
        SET_DATA(vm, GET_REG(vm,ESP), GET_REG(vm,EIP) + get_ins_size(OP_CALL_REG));

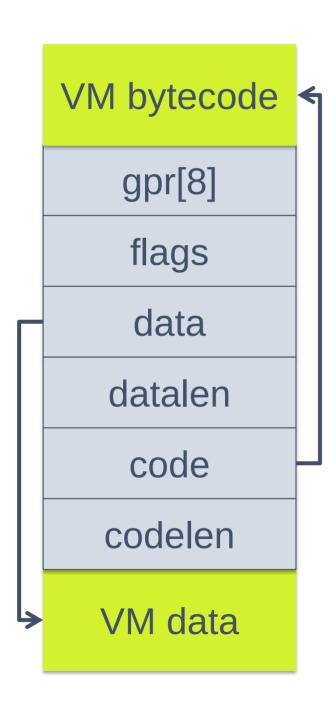
        //Next we set EIP to the target
        SET_REG(vm,EIP,GET_REG(vm,reg));
} else {
        usart_print_P(PSTR("Oops!\r\n"));
        wait_enter();

        vm->flags.interrupted = 1;
}
```

Stack pointer underflow in CALL if SP=0

## **Exploitation?**





- 1. Underflow allows modifying *code* and *codelen*
- 2. Debugger allows *loading* new code into *code*

*Arbitrary write* = *code execution* 



#### Revenge

The same manager that last time demanded field upgradable software is now asking the development team for an explanation as to why so many users have been able to hack their own fridge. The manager is also asking the legal department if they could sue every single user, but they responded that users are free to do as they want with their own equipment.

This is not acceptable, so the manager threatens to fire everybody unless they solve this major issue before coming Monday. How they resolve it is up to them, as long as it is sorted in the given time frame.

But is the solution sufficient?

Keep in mind that FI can be risky. If you brick your Arduino the game is over. Hence, you should try this challenge after you are done with the other challenges.

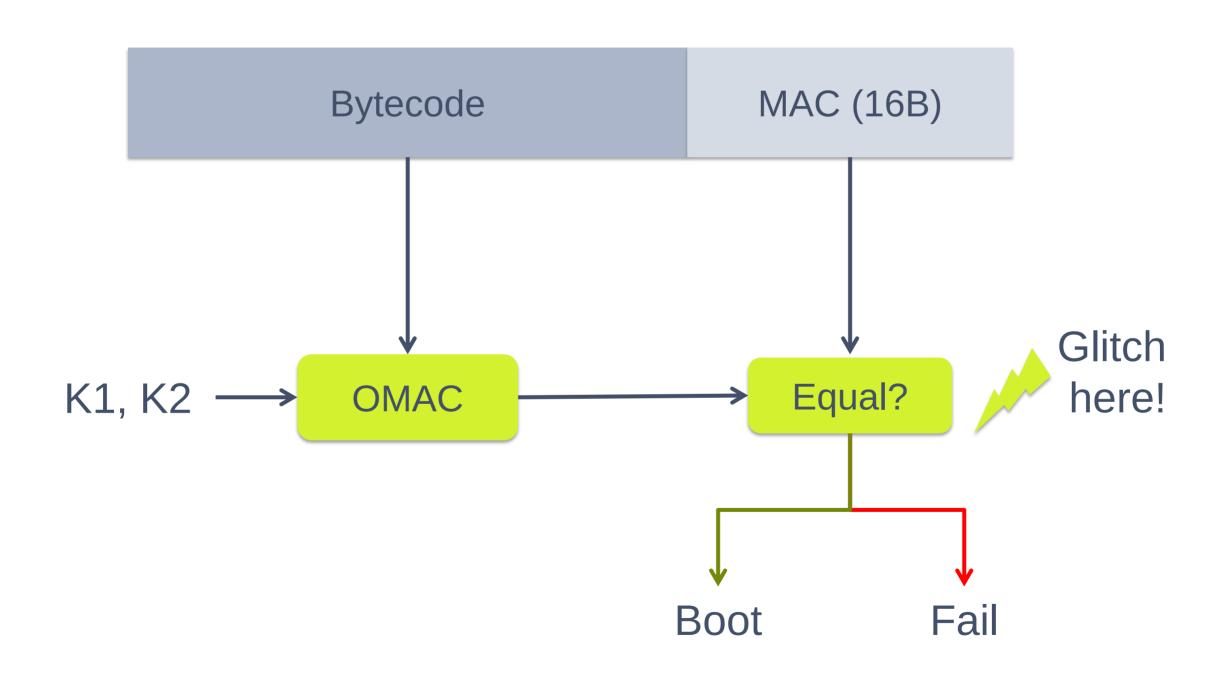
Challenge

R example.hex

solution.hex

#### **Authenticated code**





## Alternative approach (Gijs)



- 1. Load example authenticated code
- 2. Cause fault during execution (voltage glitch)
  - VM enters debugger!
- 3. Load final code through debugger

## Alternative approach (HydraBus)

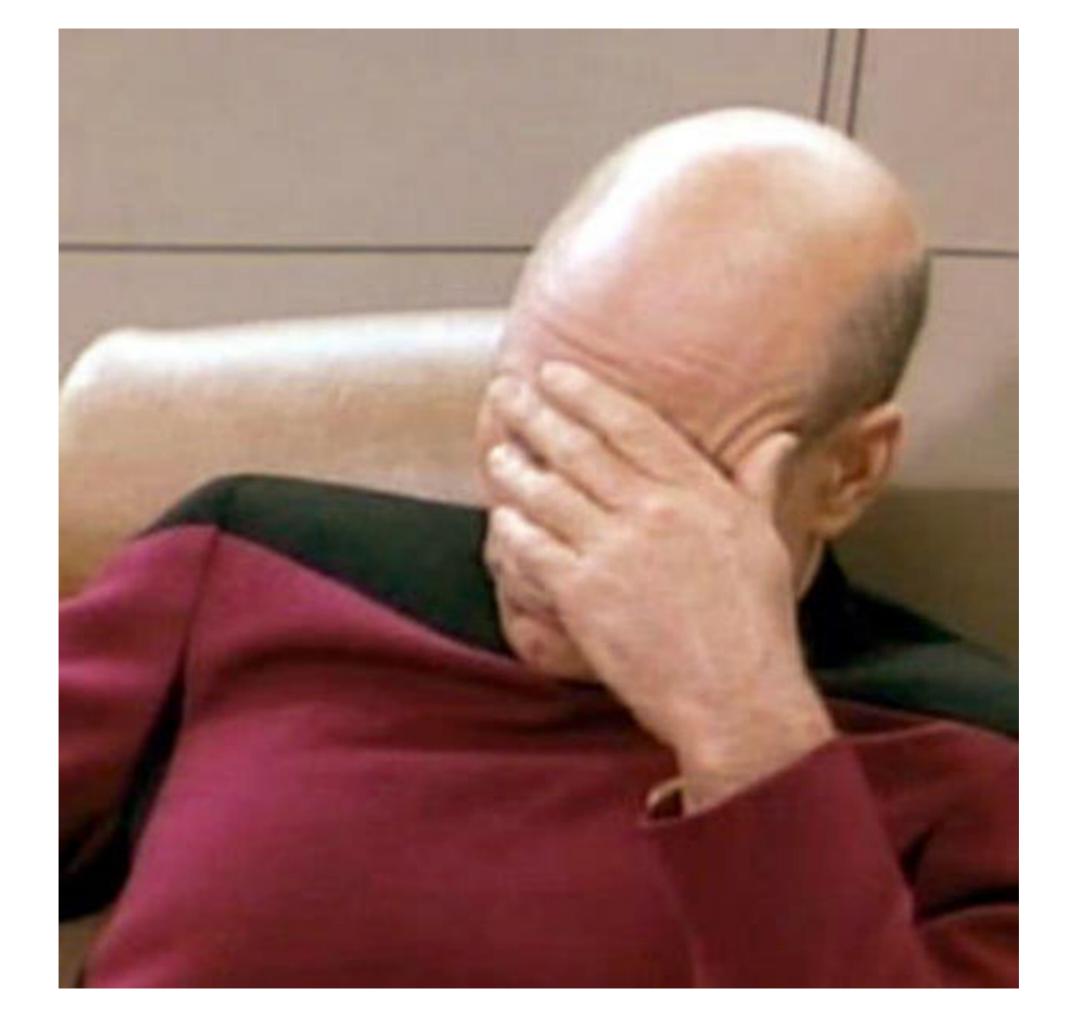


- 1. Tinker with provided binary blob
  - Modify final padding bytes
  - Bit-flip different parts
  - O ...
- 2. Find out tail not authenticated  $\rightarrow$  run VM code!

## **But...** how is that possible?



```
void omac(void *dest, const void *msg, uint16_t msglength b,
                     const void *key){
       omac_init(dest);
       while(msglength b>128){
                                                          These are bits!
           omac_next(msg, key, dest);
           msg = (uint8_t^*)msg + 16;
           msglength b -= 128;
        omac_last(msg, msglength b, key, dest);
unsigned char authenthcate_payload(unsigned char* app code, uint16_t len,
    uint8_t mac[16] = {0};
    uint8_t key[16] = \{0xac, 0x31, 0x82, 0x0f, 0xdc, 0xf4, 0xf5, 0x43, 0xac\}
    uint8_t kex[16] = \{0x48, 0x43, 0x6d, 0x71, 0x44, 0x5e, 0xc2, 0x11, 0x0\}
    omac(mac, app code, len - 16, key);
                   And these were bytes!
```





#### Emergency Transmitter

We captured a crazy guy aiming a LED at planes passing by. We believe he is a spy from the Republic of Wadiya. Your task is to reverse how the device works and extract the keys without analyzing power or electromagnetic traces.

Good luck random internet player!

Note: This challenge can be solved without fancy hardware. You can check if you got the right flag (key) by encrypting the input and comparing it against the output.

#### R Challenge

```
====== Jungle Assistance System V1.0 ======

This board will help you get out of the jungle in no time!
Write a message of maximum 16 bytes asking for help, the message
will be transmitted _encrypted_ using the LED and a secret key.
The key will remain secure even if the JAS falls into enemy
hands (We hope so).

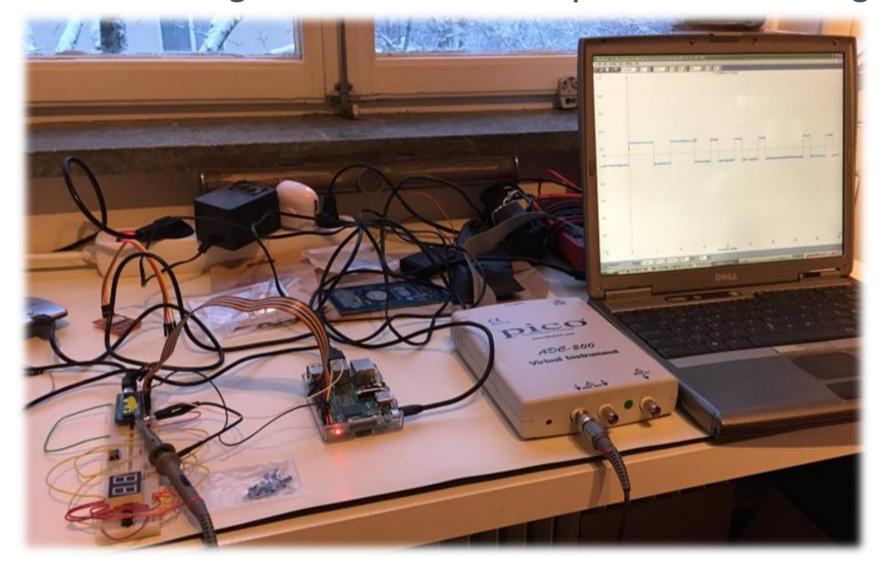
As the LED is not powerful enough please aim carefuly.

>> ■
```

## **Emergency Transmitter**



- Write 16 characters.... LED blinks... that's it.
- Rigol... Morse Code! (decoder not included)
- Create something to read and interpret the blinking LED



Taken from Calle Svensson @ZetaTwo

## Abusing the transmitter...



Several thousands attempts were omitted

- At some point, our input starts appearing in the output (Red)
- But we are also able to corrupt the output (Yellow)
- But not always... (Brown)

#### Feature 1



```
/**
226
      * @brief UART receive interruption.
227
228
229
     * Adds the received characters to te inBuffer and signals
     * when a LF character is received so the command can be
230
231
      * parsed.
232
      */
233
     ISR(USART_RX_vect)
234
             uint8 t data;
235
             data = UDR0;
236
237
238
             /* CHR_LF signals end of command */
             if (data == CHR LF) {
239
                      pos inbuffer = 0;
240
                      parse_flag = 1;
241
242
             } else if (pos inbuffer < INBUFFER LEN) {
243
                     inbuffer[pos inbuffer] = data;
244
245
                      pos inbuffer++;
246
247
     - }-
```

The interrupt is not disabled while processing the input

#### Feature 2



```
Power ON
      >> \n
      9b 83 0d ...
      >> \n
      13 da 39 ...
      Is it deterministic?
10
    Reboot
12
      >> \n
13
      9b 83 0d ...
15
      >> \n
      13 da 39 ...
16
17
      Is Deterministic!
18
19
20
    Reboot
      >> 9b 83 0d ... \n
21
22
      13 da 39 ...
23
      Input Buffer = Output Buffer
24
```

Pressing enter twice yields different outputs,
But is deterministic



The input buffer is used as output buffer We saved 16 bytes of RAM!



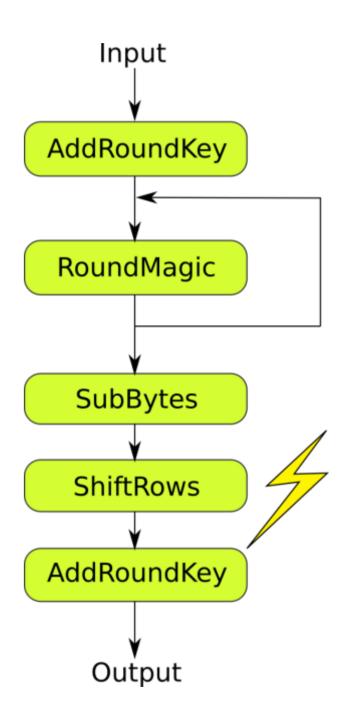
The input buffer is used as output buffer Is the internal state of the cipher

HydraBus mental process

#### **Exploit**

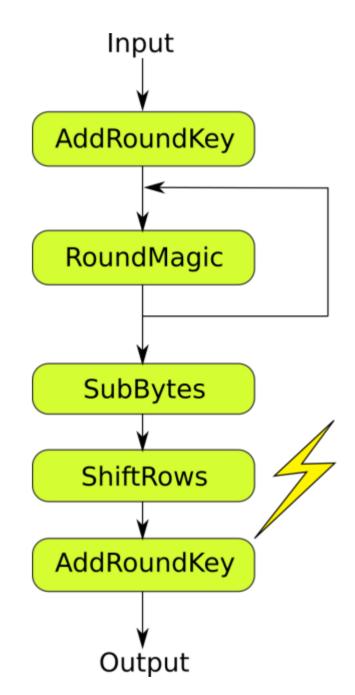


- Input buffer is reused for internal state.
- Interruption is not disabled.
- DFA.
- Not really... we control the value and position of the fault.
- Simplified Math.
- Inject 00, Sk = Output.
- Get key by reversing AES key scheduling.



## **Timing**

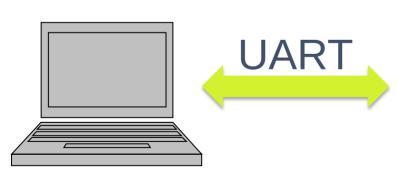




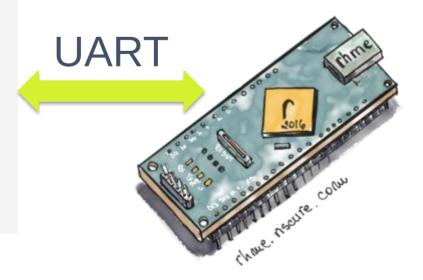
#### **Exploitation**



```
def attack(crack_byte, delay):
    conn.write("%s\n" % (delay,))
    rsp = wait_prompt(conn)
    rsp = rsp.split()
    rsp = rsp[1:len(rsp)]
    (correct, wrong, too_soon, too_late) = analize(crack_byte, rsp)
    if too_soon == True:
        too_soon = bcolors.WARNING + str(too_soon) + bcolors.ENDC
    if too_late == True:
        too_late = bcolors.WARNING + str(too_late) + bcolors.ENDC
    print(("Correct: %s, Corrupted: " + bcolors.FAIL + "%s" + bcolors.ENDC + " Too soon: %s, Too late: %s") %
        (correct, wrong, too_soon, too_late))
    print("%s" % (e,))
    return too_soon
```







#### Timing is everything



- The amount of bytes that can be injected depends on the processor clock speed and baud rate.
- Challenge was clocked down for this reason.
- However, the last bytes were difficult to obtain.
- Brute forcing may be required

#### **Different Approaches**



- HydraBus Common DFA on AES. Tried to attack AddRoundKey
- Balda Attack on last add round key + Brute force
- Riscure Failed normal DFA, attacked last round.
- Nobody? DFA with hardware



#### SCA2



Side Channel Analysis - 100pts

#### Piece of scake

This is an easy SCA challenge using a cipher implementation without any SCA or DFA countermeasures. Find the key used to encrypt and decrypt messages. Please, consider both SCA and DFA attacks.

To encrypt a message, send the letter 'e' followed of 16 bytes. To decrypt a message, send the letter 'd' followed of 16 bytes.

R Challenge

Side Channel Analysis - 300pts

#### Still not scary...

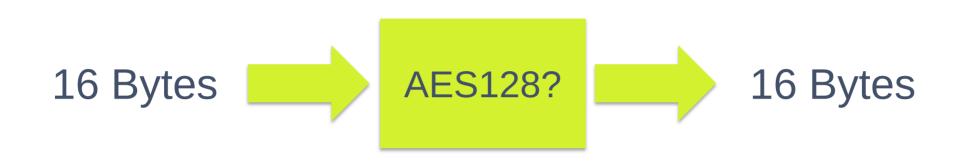
We added a simple countermeasure to the previous challenge.

Will you be able to break it?

Challenge

#### SCA2 – Still Not Scary

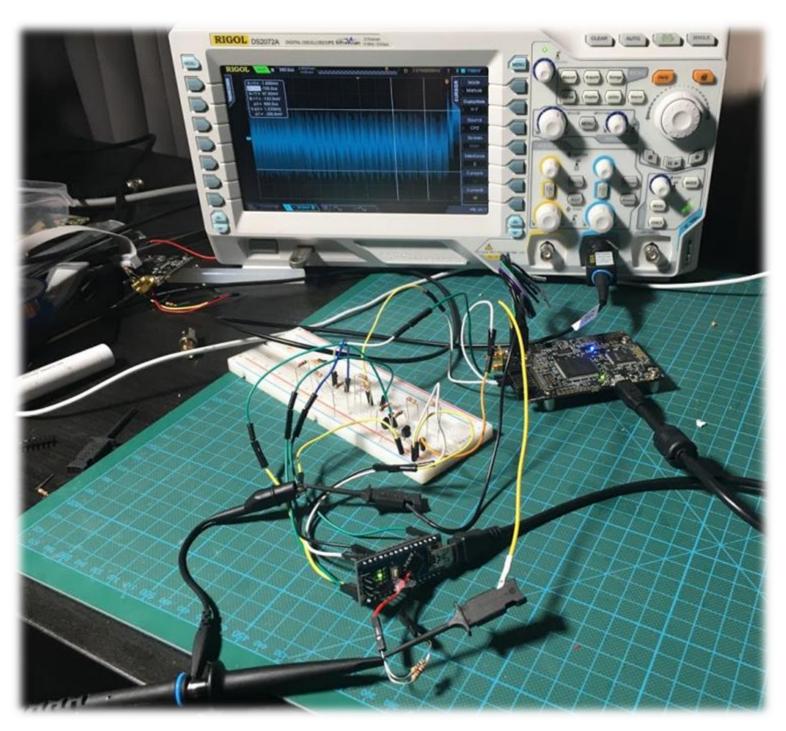




- No fancy command line.
- Solution with ChipWhisperer
- Why not Riscure tools? Too easy

# **Traces Acquisition**

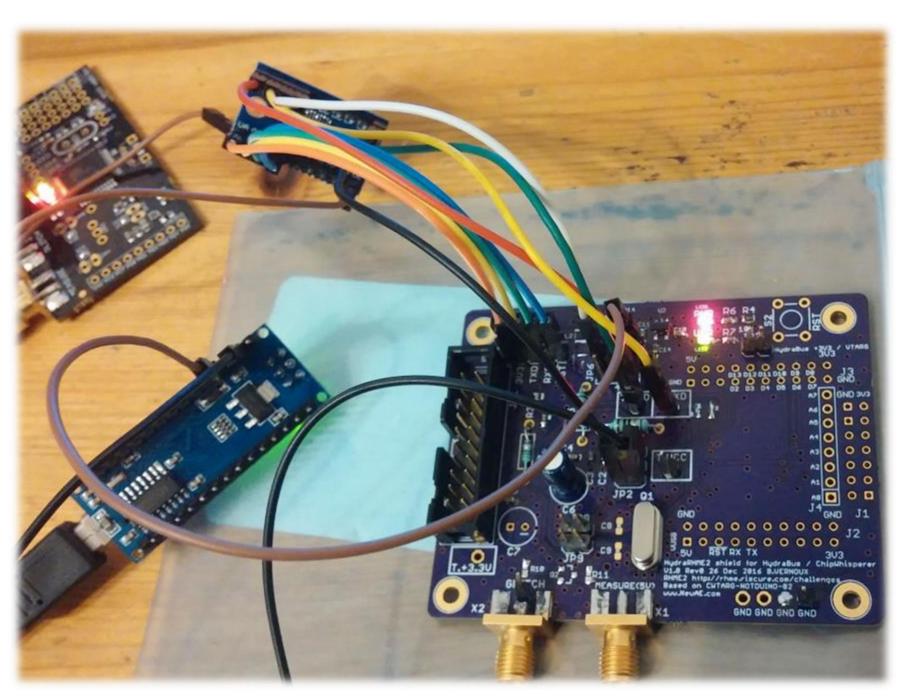




Taken from LiveOverflow @LiveOverflow

# **Traces Acquisition**

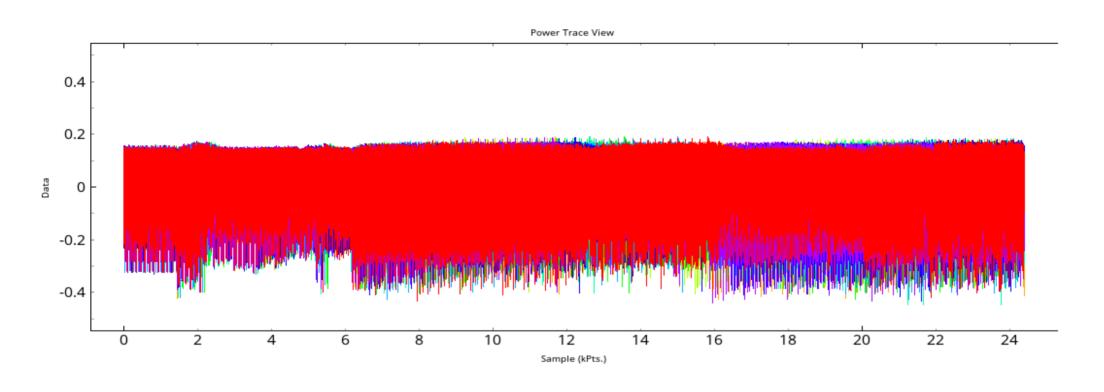


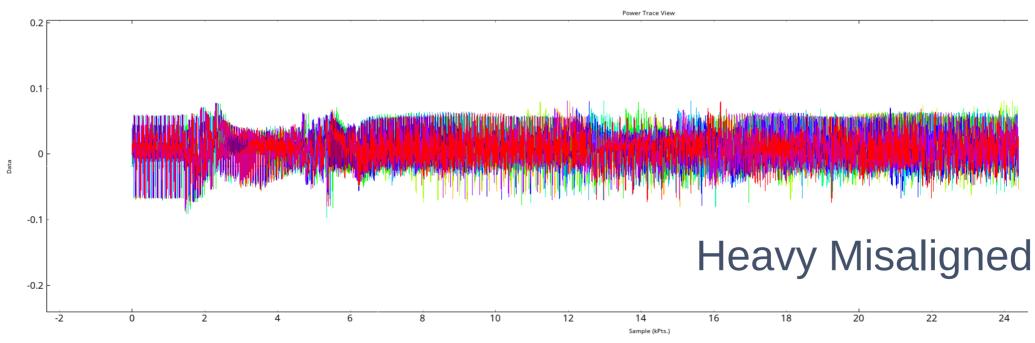


Taken from HydraBus @HydraBus

# **Analysis**







Taken from HydraBus @HydraBus

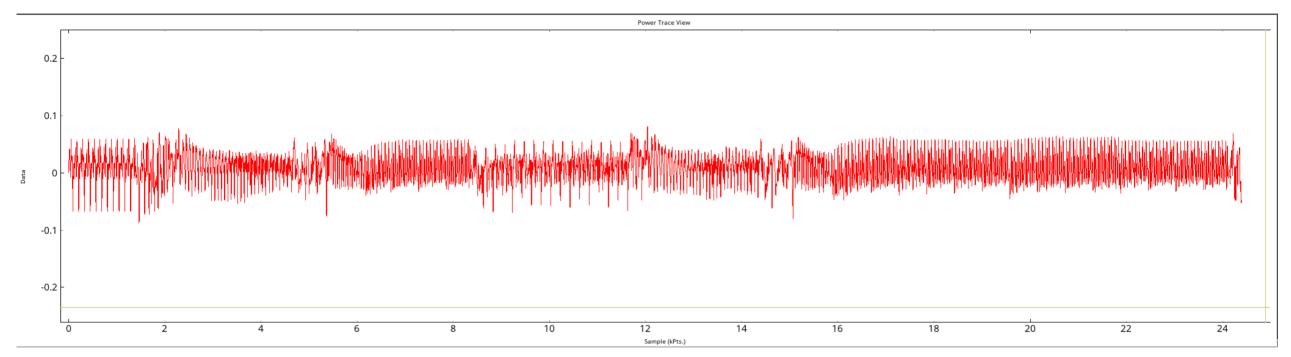
### **Analysis**

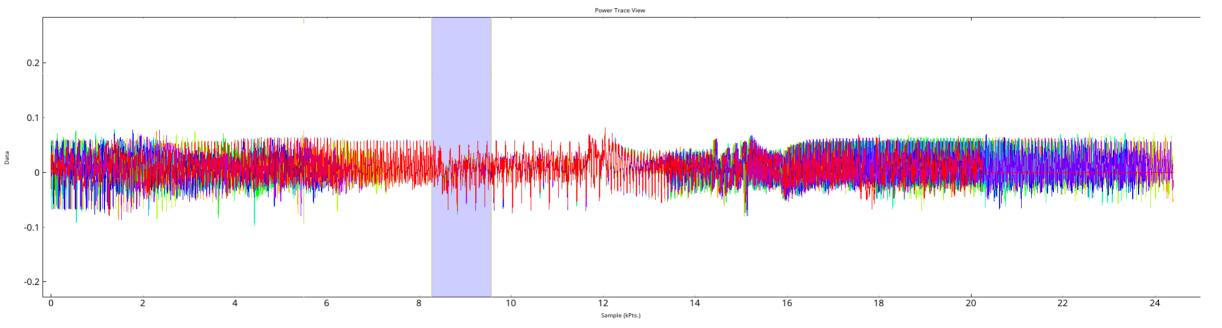


- Attack plan (CW Tutorials):
  - Align Resync: Sum-of-Difference
  - Attack HW:SBox output (SCA1)
  - Fail
- CW can only capture ~24k points
- Sbox is out of capture
- Can't drop unaligned traces?
- New plan:
  - Align around key addition Resync: Sum-of-Difference
  - Attack HW: AddRoundKey Output

# Alignment

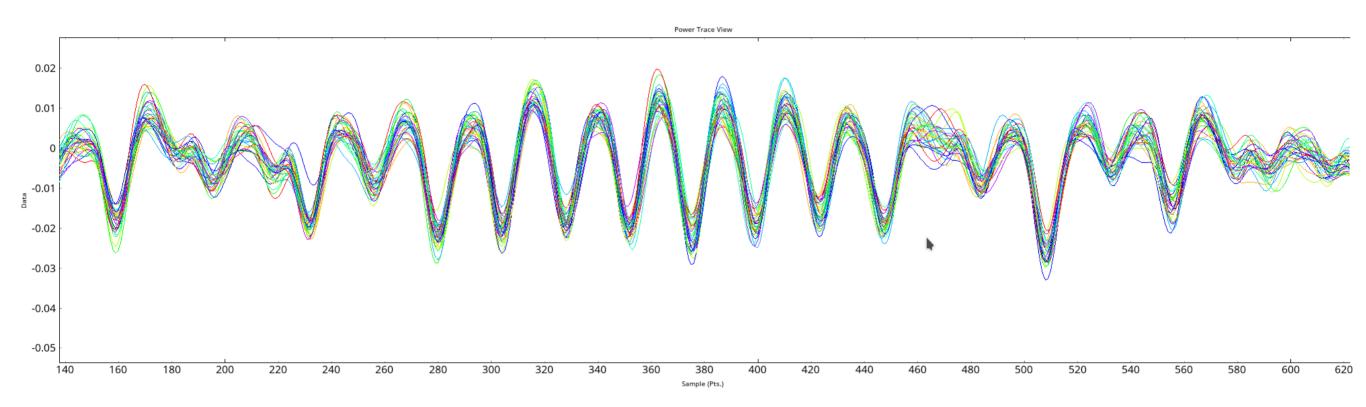






# **Alignment Result**





# **Attack - Key Addition**



	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
PGE	9	175	248	152	94	149	1722	245	179	99	19	130	180	32	176	253
0	6B	F6	C7	A6	4D	18	89	0D	79	33	0E	86	00	D2	96	A8
	0.9749	0.9826	0.9873	0.9747	0.9875	0.9672	0.9858	0.9852	0.9843	0.9802	0.9839	0.9732	0.9363	0.9843	0.9690	0.9882
1	94	09	38	59	B2	E7	76	F2	86	CC	F1	79	FF	2D	69	57
	0.9749	0.9826	0.9873	0.9747	0.9875	0.9672	0.9858	0.9852	0.9843	0.9802	0.9839	0.9732	0.9363	0.9843	0.9690	0.9882
2	F0	FF	21	BF	AB	FE	6F	FF	9F	2A	00	FF	51	FF	8F	5F
	0.8853	0.9394	0.8961	0.9177	0.9047	0.9249	0.9030	0.9200	0.9159	0.9199	0.9222	0.9186	0.9182	0.9439	0.8934	0.8869
3	0F	00	DE	40	54	01	90	00	60	D5	FF	00	AE	00	70	A0
	0.8853	0.9394	0.8961	0.91 <b>7</b> 7	0.9047	0.9249	0.9030	0.9200	0.9159	0.9199	0.9222	0.9186	0.9182	0.9439	0.8934	0.8869
4	8C	EF	FF	00	FF	00	72	14	00	FF	E8	7F	B7	34	FF	AC
	0.8768	0.8977	0.8647	0.8851	0.9015	0.9096	0.8937	0.9160	0.8887	0.8905	0.8969	0.9091	0.8779	0.9125	0.8878	0.8748
5	73	10	00	FF	00	FF	8D	EB	FF	00	17	80	48	CB	00	53
	0.8768	0.8977	0.8647	0.8851	0.9015	0.9096	0.8937	0.9160	0.8887	0.8905	0.8969	0.9091	0.8779	0.9125	0.8878	0.8748
6	8D	08	20	4E	55	1C	FF	04	6E	DB	0F	9F	50	CA	7E	00
	0.8702	0.8559	0.8571	0.8675	0.8625	0.8580	0.8604	0.8728	0.8582	0.8820	0.8826	0.9088	0.8758	0.8380	0.8592	0.8688
7	72	F7	DF	B1	AA	E3	00	FB	91	24	F0	60	AF	35	81	FF
	0.8702	0.8559	0.8571	0.8675	0.8625	0.8580	0.8604	0.8728	0.8582	0.8820	0.8826	0.9088	0.8758	0.8380	0.8592	0.8688
8	D4	D3	39	41	DA	EF	6E	EA	9E	16	16	71	40	3A	8E	80
	0.8392	0.8443	0.8468	0.8584	0.8269	0.8563	0.8541	0.8708	0.8432	0.8699	0.8603	0.8696	0.8691	0.8281	0.8579	0.8688
9	2B	2C	C6	BE	25	10	91	15	61	E9	E9	8E	BF	C5	71	7F
	0.8392	0.8443	0.8468	0.8584	0.8269	0.8563	0.8541	0.8708	0.8432	0.8699	0.8603	0.8696	0.8691	0.8281	0.8579	0.8688
10	F4	93	87	7F	58	08	1A	09	15	D4	6E	91	F7	5C	9E	AA
	0.8273	0.8339	0.8356	0.8556	0.8198	0.8386	0.8401	0.8421	0.8385	0.8422	0.8594	0.8563	0.8489	0.8018	0.8421	0.8335

#### **Brute Force!**



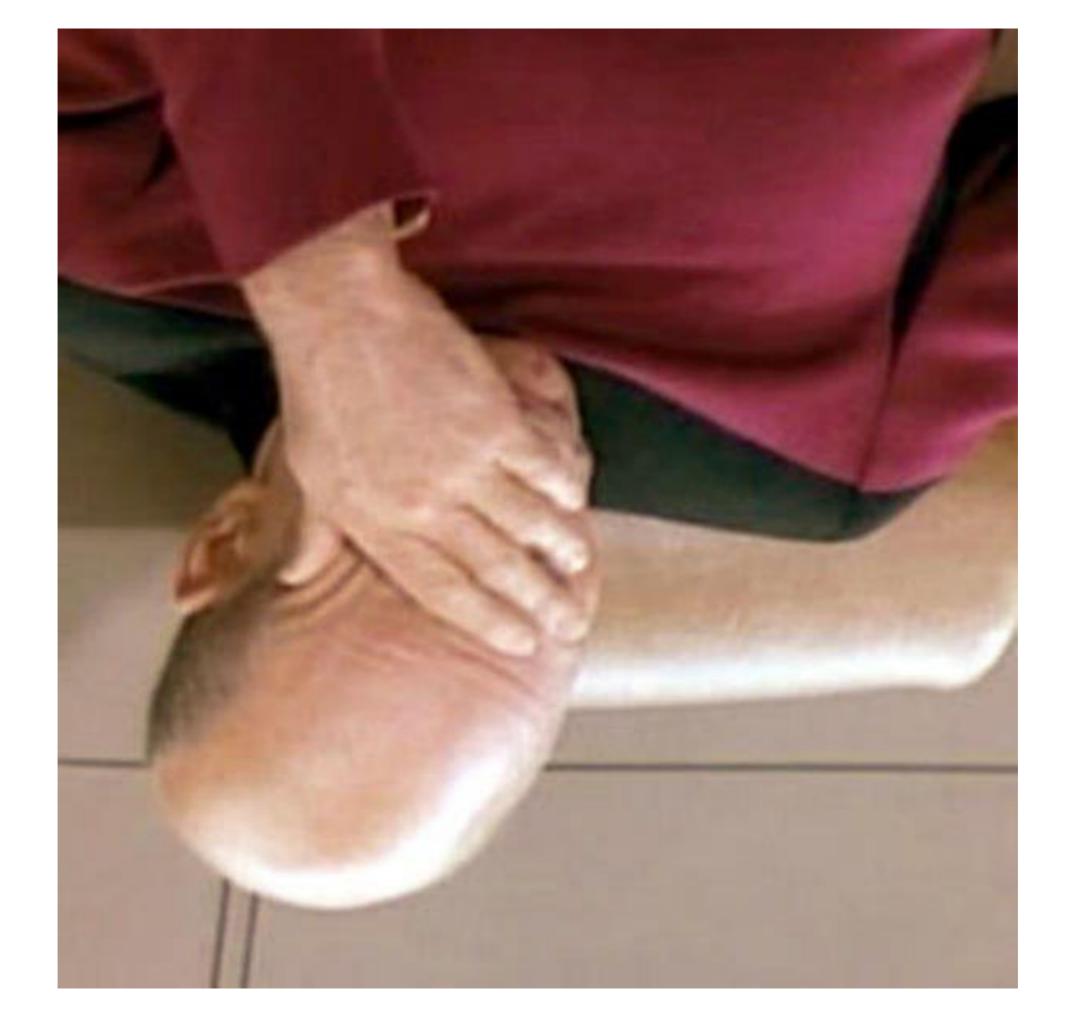
```
#!/usr/bin/python
from Crypto.Cipher import AES
import binascii
plaintext = binascii.a2b hex("A7D961D8083DF362D003A5201C665AB3")
output = binascii.a2b hex("FC73ADEF2B110C8B770DA196E60D6454")
def check key(key):
    cipher = AES.new(key, AES.MODE ECB)
    tmp = cipher.encrypt(plaintext)
    if tmp == output:
        print("Key found")
        print(binascii.b2a hex(key))
        exit(0)
byte0 = [0x6b, 0x94]
byte1 = [0xf6, 0x09]
byte2 = [0xc7, 0x38]
byte3 = [0xa6, 0x59]
byte4 = [0x4d, 0xb2]
byte5 = [0x18, 0xe7, 0xfe]
byte6 = [0x89, 0x76]
byte7 = [0x0d, 0xf2]
byte8 = [0x79, 0x86]
byte9 = [0x33, 0xcc]
bytel0 = [0x0e, 0xf1]
bytell = [0x86, 0x79]
byte12 = [0x00, 0xff, 0x51, 0xae, 0xb7, 0x48, 0x50, 0xaf]
byte13 = [0xd2, 0x2d, 0xff, 0x00]
byte14 = [0x96, 0x69]
byte15 = [0xa8, 0x57]
l0 = len(byte0)
l1 = l0 * len(byte1)
```

```
andres@kali-andres:~/lol/dev$ python ./bruteforce.py
Key found
940938a64d18890d79330e86482d96a8
```

### **Side Channel analysis**

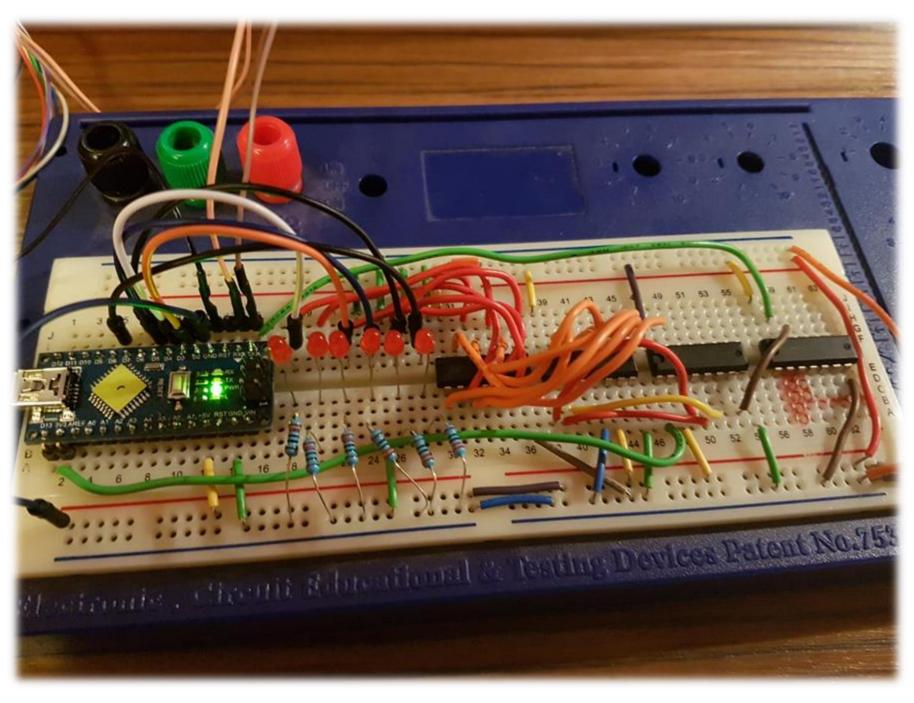


- SCA1: The trigger is on the house, no counter measures
- SCA2: No trigger, random delays
- SCA3: No trigger, random delays, dummy rounds, anti-DFA
- SCA3: Countermeasure were added after the AddRoundKey... ooops
- SCA2 and SCA3 can be solved the same way.



#### Shout-out - Ar1s

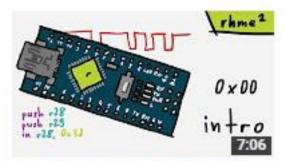




Aris Adamantiadis @aris\_ada

#### **Shout-out – LiveOverflow**

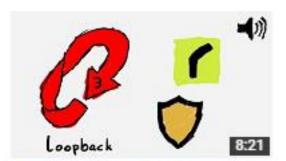




Riscure Embedded Hardware CTF setup and introduction - rhme2 2,960 views • 2 months ago



Using UART / Serial to interact with an embedded device -3,096 views • 2 weeks ago



riscure embedded hardware CTF is over - loopback 0x03

2,389 views • 2 weeks ago



Taken from LiveOverflow @LiveOverflow



Start reverse engineering AVR -Memory Map and I/O Registers ...

2,354 views · 3 days ago

CC



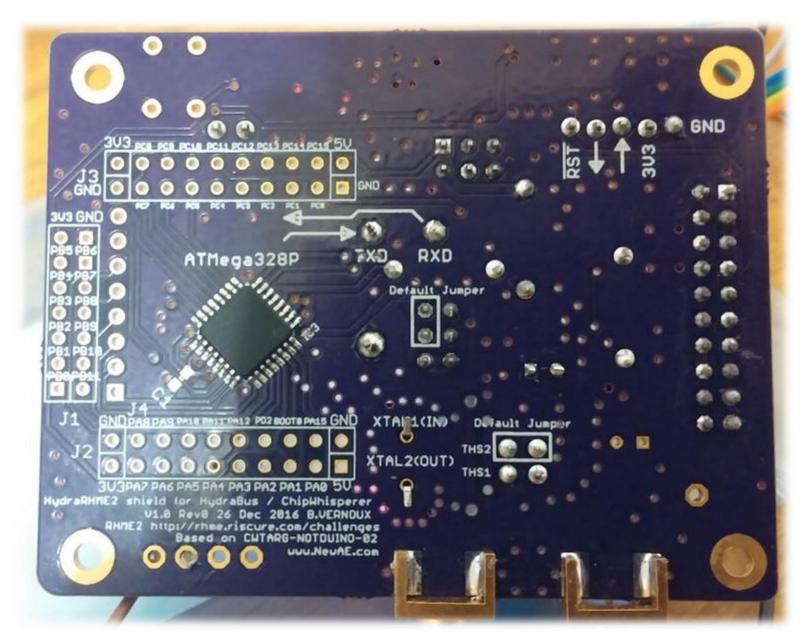
SHA1 length extension attack on the Secure Filesystem - rhme2

2,955 views • 1 week ago

CC

# **Shout-out - HydraBus**





Taken from HydraBus @HydraBus

#### **Shout-out - MrMacete**

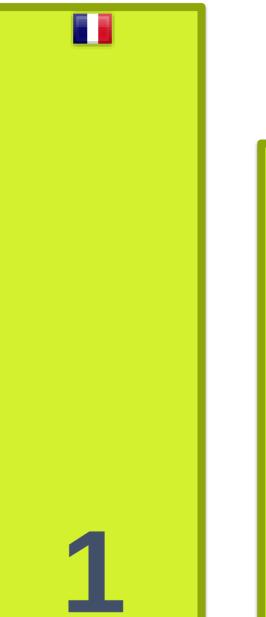


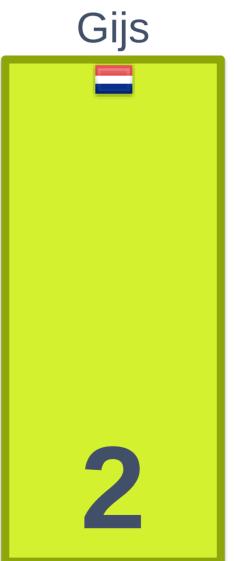
```
MOVL R3, 0x0002
                  MOVL R5, 0x0038
                  CALL R5
                 MOVL R5, 0x018c
                  CALL R5
                  JNZ 0x0184 ;[a]
            0x13d ;[c]
           MOVL R5, 0x01b0
            CALL R5
            JNZ 0x0184 ;[a]
      0x146 ;[d]
     MOVL R5, 0x01cc
      CALL R5
      JNZ 0x0184 ;[a]
0x14f ;[e]
MOVL R5, 0x020c
CALL R5
JNZ 0x0184 ;[a]
```

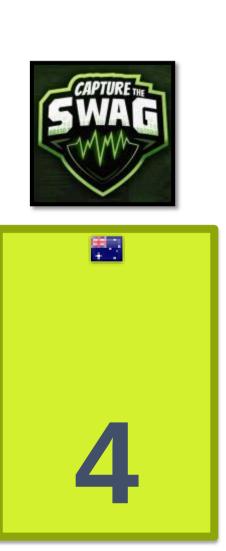












SOS1

Balda

#### **Conclusions**



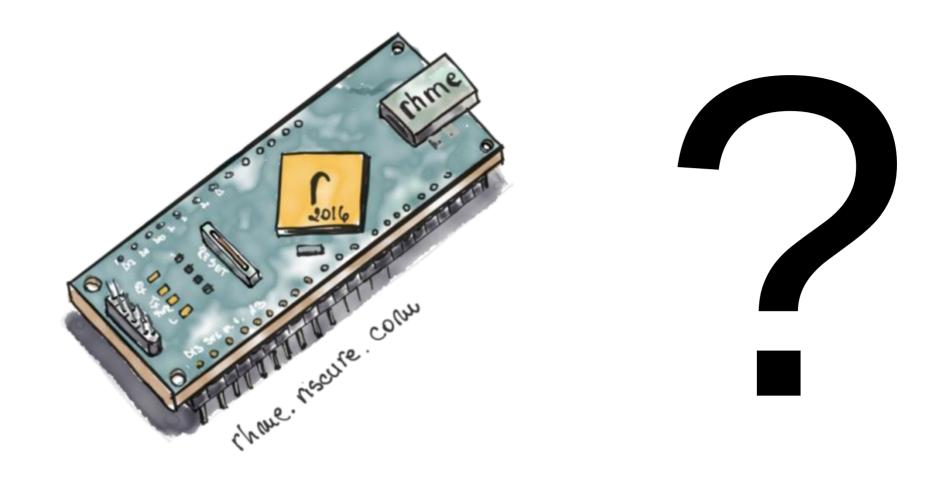
- Fewer players than boards, but high skills and motivation
  - Long-running contest gives a chance to individuals
- Preparing and running the CTF was fun for the team
- But earlier prep required for next year
  - You can always do more testing!
  - There are always unintended solutions ⊗
  - Delivery to some parts of the world is SLOW
- Good feedback received
  - Also improvement points for the challenges ;-)

#### Want to know more?



- 1. Follow @riscure for updates
  - News on RHMe3 (~ November 2017)
  - Other embedded security news

- 2. Check out <a href="https://github.com/riscure/rhme-2016">https://github.com/riscure/rhme-2016</a>
  - Challenge binaries and code so you try them out
  - Links to write-ups in case you get stuck



We're hiring!! Get in touch if you're interested!

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